

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re: Mario Au et al.

Serial No.: To Be Assigned

Filed: Concurrently Herewith

For: FIFO MEMORY DEVICES HAVING MULTI-PORT CACHE MEMORY ARRAYS
THEREIN THAT SUPPORT HIDDEN EDC LATENCY AND BUS MATCHING
AND METHODS OF OPERATING SAME

Date: July 3, 2003

Mail Stop Patent Application
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

PRELIMINARY AMENDMENT

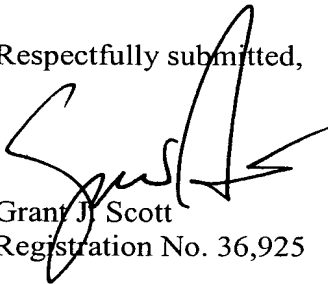
Dear Sirs:

Please enter the following Preliminary Amendment before examining the present application.

In the Claims:

Please cancel Claims 22-58.

Respectfully submitted,



Grant J. Scott
Registration No. 36,925

Correspondence Address:



20792

PATENT TRADEMARK OFFICE

CERTIFICATE OF EXPRESS MAILING

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Candi L. Riggs

Date of Signature: July 3, 2003